

Course Specifications

Course Title:	Fault Tolerant Computing
Course Code:	503539-3
Program:	Bachelor in Computer Engineering
Department:	Department of Computer Engineering
College:	College of Computers and Information Technology
Institution:	Taif University











Table of Contents

A. Course Identification	3
6. Mode of Instruction (mark all that apply)	3
B. Course Objectives and Learning Outcomes	3
1. Course Description	3
2. Course Main Objective	3
3. Course Learning Outcomes	3
C. Course Content	4
D. Teaching and Assessment	4
1. Alignment of Course Learning Outcomes with Teaching Strate Methods	gies and Assessment 4
2. Assessment Tasks for Students	4
E. Student Academic Counseling and Support	5
F. Learning Resources and Facilities	5
1.Learning Resources	5
2. Facilities Required	5
G. Course Quality Evaluation	5
H. Specification Approval Data	6

A. Course Identification

1.	Credit hours: 3		
2.	Course type		
a.	University College Department Others		
b.	Required Elective		
3.	Level/year at which this course is offered: 10/5		
	4. Pre-requisites for this course (if any): 503528-3		
5.	5. Co-requisites for this course (if any): None		

6. Mode of Instruction (mark all that apply)

No	Mode of Instruction	Contact Hours	Percentage
1	Traditional classroom	3	100%
2	Blended		
3	E-learning		
4	Distance learning		
5	Other		

7. Contact Hours (based on academic semester)

No	Activity	Contact Hours
1	Lecture	45
2	Laboratory/Studio	
3	Tutorial	
4	Others (specify)	
	Total	45

B. Course Objectives and Learning Outcomes

1. Course Description

This course covers basic concepts of dependable computing. Reliability of non-redundant and redundant systems. Dealing with circuit-level defects. Logic-level fault testing and tolerance. Error detection and correction. Diagnosis and reconfiguration for system-level malfunctions. Degradation management. Failure modeling and risk assessment.

2. Course Main Objective

- 1. Understand Dependability measures and modeling.
- 2. Discuss how to deal with low level impairments.
- 3. Understand various hardware implementation schemes.

3. Course Learning Outcomes

CLOs	Aligned PLOs
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1	Knowledge and Understanding	
1.1	Recognise the basic concepts of dependable computing	K1
1.2	Describe the reliability of nonredundant and redundant systems	K1
1.3		
1		
2	Skills:	
2.1	Identify and interpret the various circuit level defects	S1
2.2	Estimate and classify all logic level fault testing and tolerance.	S1
2.3	Discuss error detection and correction.	S3
2		
3	Values:	
3.1		
3.2		
3.3		
3		

C. Course Content

No	List of Topics	Contact Hours
1	Introduction to Fault Tolerant Computing	4
2	Dependability measures in fault tolerant	4
3	Fault tolerant combinational modeling	4
4	Fault tolerant State space modeling	4
5	Defect avoidance and circumvention	4
6	Fault testing and Fault masking	5
7	Error detection and Error correction	5
8	Malfunction diagnosis and tolerance	5
9	Degradation allowance and management	5
10	Failure confinement	5
11		
12		
13		
14		
	Total	45

D. Teaching and Assessment

1. Alignment of Course Learning Outcomes with Teaching Strategies and Assessment Methods

Code	Course Learning Outcomes	Teaching Strategies	Assessment Methods
1.0	Knowledge and Understanding		
1.1	Recognise the basic concepts of dependable computing	Lecture Discussion Problem Solving	Written Exams Quizzes Assignments
1.2	Describe the reliability of nonredundant and redundant systems	Lecture Discussion Problem Solving	Written Exams Quizzes Assignments

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2.0	Skills		
2.1	Identify and interpret the various circuit level defects	Lecture Discussion Projects	Written Exams Quizzes Assignments Oral Test Project Practical Test
2.2	Estimate and classify all logic level fault testing and tolerance.	Lecture Discussion Projects	Written Exams Quizzes Assignments Oral Test Project Practical Test
	Discuss error detection and correction.	Lecture Discussion Projects	Written Exams Quizzes Assignments Oral Test Project Practical Test
3.0	Values		
3.1			
3.2			
		-	

2. Assessment Tasks for Students

#	Assessment task*	Week Due	Percentage of Total Assessment Score
1	Assignments	Continues	5%
2	Midterm Exam	7	20%
3	Project	14	15%
4	Quizzes	Continues	10%
5	Final Exam	16	50%
6			
7			
8			

^{*}Assessment task (i.e., written test, oral test, oral presentation, group project, essay, etc.)

E. Student Academic Counseling and Support

Arrangements for availability of faculty and teaching staff for individual student consultations and academic advice :

- Teaching staff provide at least 6 office hours for students to help them in the course as well as in any other academic issues.
- Consultation can also be done 24 hours/7days through university Edugate (Tawasol)

F. Learning Resources and Facilities

1.Learning Resources

Required Textbooks	Pradhan, D.K, Fault-Tolerant Computer System Design, Prentice Hall, 11 th Ed, 1996.
Essential References Materials	Reliable Computer Systems: Design and Evaluation (second edition) by D. P. Siewiorek and R. S. Swarz, Digital Press.
Electronic Materials	http://class.ece.iastate.edu/arun/Cpre545/references.htm
Other Learning Materials	Fault Tolerance in Distributed Systems, Pankaj Jalote, PTR Printice Hall, 1994.

2. Facilities Required

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Item	Resources	
Accommodation (Classrooms, laboratories, demonstration rooms/labs, etc.)	Traditional Classrooms, Laboratories	
Technology Resources (AV, data show, Smart Board, software, etc.)	White Board. Datashow, software	
Other Resources (Specify, e.g. if specific laboratory equipment is required, list requirements or attach a list)		

G. Course Quality Evaluation

Evaluation Areas/Issues	Evaluators	Evaluation Methods
Extent of achievement of course learning outcomes	Students	Indirect (Survey)
Effectiveness of teaching and assessment	Students	Indirect (Survey)
Extent of achievement of course learning outcomes	Faculty	Course Report

Evaluation areas (e.g., Effectiveness of teaching and assessment, Extent of achievement of course learning outcomes, Quality of learning resources, etc.)

Evaluators (Students, Faculty, Program Leaders, Peer Reviewer, Others (specify)

Assessment Methods (Direct, Indirect)

H. Specification Approval Data

Council / Committee	Computer Engineering Council / Curriculum Committee
	16
Reference No.	10
Date	04/02/2019



